



F1C100A

Overview

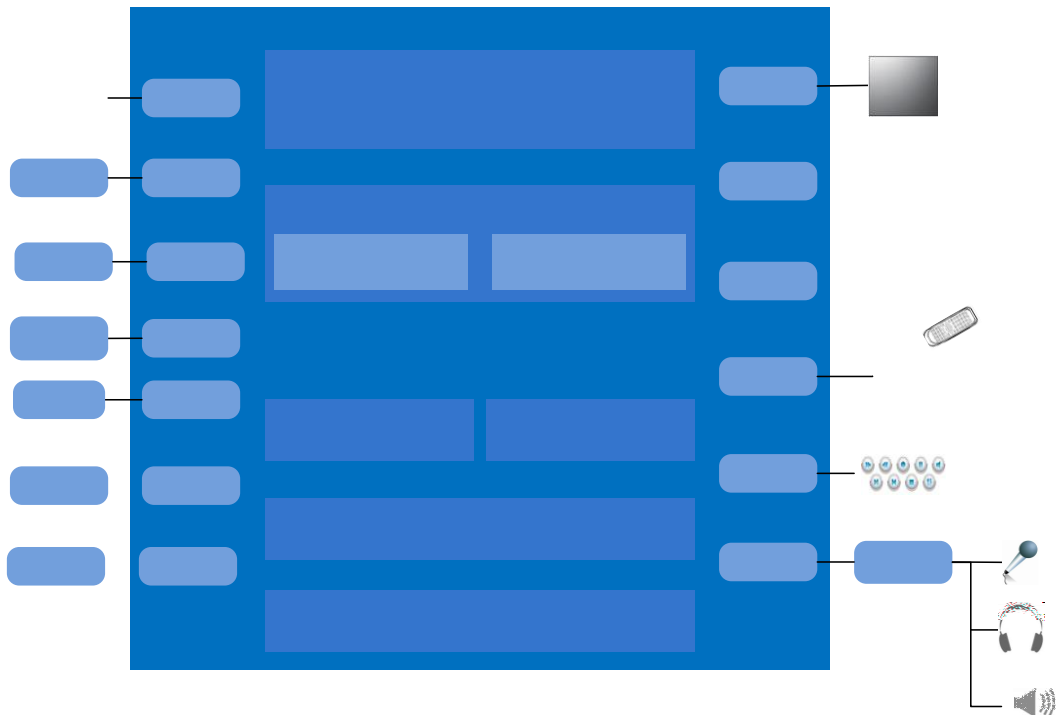
ARM926EJ-S

cost-effective and lower power consumption.

What's more important,

eLQFP128 package with Audio Codec, CVBS input

Block Diagram





Feature

CPU

-
- ARM926EJ-S
- 16KB D-Cache/ 32KB I-Cache

VPU

-
- Up to 1920*1080@30fps
-
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- Up to 1280*720@30fps
-

DPU

-
- CVBS Input
-
-

Audio

- Audio Codec
-

Memory

-
-

Peripherals

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-
-

R-TP Controller

- 4-wire resistive TP interface
- 2 points

Boot Devices

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Ultra-low System Power Consumption

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- Smart Backlight:

Package

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Application



Benefits

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Contact

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